CAV Workshop Invited Speakers (July 14-15)

Satisfiability Modulo Theories (SMT)
- Viktor Kuncak, EPFL
- Bud Mishra, NYU

Exploiting Concurrency
  Efficiently and Correctly ((IEC)^2)
- Tom Ball, MSR Redmond
- Martin Berzins, Univ of Utah
- Guy Blloch, CMU
- Johannes Gehrke, Cornell
- Murali Talupur, Intel
- Richard Vuduc, GaTech

Numerical Software Verification (NSV)
- James Demmel, Berkeley
- Thomas Gawlitza, INRIA Rhônes-Alpes

SPIN
- Jasmin Fisher, MSR Cambridge
- John Regehr, Univ of Utah

Frontiers in Analog
  Synthesis/Verification (FAC)
- Christoph Grimm, TU Wein
- Mark Horowitz, Stanford
- Robert Hum, Mentor Graphics
- Mahai Marcu, Agilent
- Rob Rutenbar, Illinois
- Gregory Taylor, Intel

Parallel Distributed Methods
  in verification (PDMC)
- Lubos Brim, Brno
- Orna Grumberg, Technion
- Kim G. Larsen, Aalborg
- Jaco van de Pol, Twente
- Moshe Vardi, Rice

Practical Synthesis
  for Concurrent Systems (PSY)
- Rajeev Alur, U Penn
- Bernd Finkbeiner, Saarland U
- Viktor Kuncak, EPFL
- Doug Smith, Kestrel
- Armando Solar-Lezama, MIT

Formal Methods for
  Robotics and Automation (FM-R)
- Rajeev Alur, U Penn
- Calin Belta, Boston U
- Krishnendu Chatterjee, IST
- Oded Maier, IMAG
- Moshe Vardi, Rice

CAV Conference Highlights (July 16-20)

Invited Speakers
- Vikram Adve, Illinois
- Andy Chou, Coverity Inc
- RoI Ernst, Braunschweig U
- Vigyan Singhal, Oski Technologies

Tutorial Speakers
- Vijay Ganesh, MIT
- Ranjit Jhala, UCSD
- Shuvendu Lahiri, MSR Redmond
- Andre Platzer, CMU

CAV Technical Program Highlights
- 35 regular papers
- 20 tool papers and demo session

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Kajip Heljanko, Aalto University
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Frans Ingen, NEC Labs
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Stefan Kowalewski, RWTH Aachen
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Sriram Sankaranarayanan, University of Colorado at Boulder
Rafael Sebastian, University of Trento
Sanjit A. Seshia, University of California at Berkeley
Ofer Strichman, Technion
Muralidhar Talupur, Intel
Serdar Tasiran, Koc University
Ashish Tiwari, SRI International
Taysser Touati, LIAFA
Viktor Vafeiadis, MPI SWG
Bow-Yaw Wang, Inria and Tsinghua University

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